

**REMARKS / ARGUMENTS**

Claims 1-20 remain pending in the application. Claims 1, 12 and 20 are the only independent claims and each includes a limitation not disclosed by nor made obvious in view of the prior art.

Formal drawings will be provided in accordance with the Examiner's requirements and suggestions in the Office Action at such time as claims are indicated allowable.

For example, each of claims 1, 12 and 20 is directed to a system that includes "a plurality of heterogeneous processing nodes". The nodes are "coupled by a programmable interconnection network having a network root and a plurality of crosspoint switches".

Note that Wilson's interconnection scheme employs a single crossbar switch 51 coupled to processing units such as 46, 47 and 48. See, e.g., Wilson's Fig. 5. Other switches such as 50 are not coupled to processing units. Also, there is no use of a "network root". Thus, Wilson teaches away from the recited architecture.

Okuda is concerned with a memory device and does not disclose processing units, processor interconnections, crosspoint switches or a network root.

Applicant respectfully requests that a timely Notice of Allowance be issued in this case. If the Examiner believes a telephone conference would expedite prosecution of this application, please telephone the undersigned at 415-279-5098.

March 20, 2006  
Date

Respectfully submitted,



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